ARYAN SCHOOL OF ENGINEERING & TECHNOLOGY

Dicipline: CSE	Semester: 4th	Name of the Teaching Faculty: Deepak Kumar Sahoo	
Subject: MPMC	No of Days/Week Class Allotted: 5	Semester From date: 13.2.23 To date 23.5.23	No.of Weeks: 15

WEEK	Class Day	Theory Topics
1	1st	Introduction to MicroProcessor and Microcontroller
	2nd	Distinguiss between MicroProcessor & micro controller.
	3rd	Concept of Address bus, data bus
	4th	concept of control bus & System bus
	5th	General bus structure & block diagram
2	1st	Basic Architecture of 8085
	2nd	Signal description of 8085 Micro-Processor
	3rd	Register organisations
	4th	Distenguiss between SPR & GPR
	5th	Timing & control module
3	1st	Stack, Stack Pointer & Stack top
	2nd	Interrupts :- 8085 Interrupts
	3rd	Masking of Interrupt
	4th	SIM, RIM
	5th	Test - 1

WEEK	Class Day	Theory Topics
4	1st	Again discuss the unit - 1
	2nd	Basic Introduction to 8086 Micro Processor
	3rd	Comparison between 8085 & 8086 MicroProcess
	4th	Concept to 8086 MicroProcessor
	5th	Register Organisation of 8086
5	1st	Internal architecture of 8086
	2nd	Signal Description of 8086
	3rd	General bus operation
	4th	Physical Memory Organisation
	5th	Interrupts & Maskable & NONMaskable
6	1st	8086 Instruction Set
	2nd	Minimum Mode & Maximum Mode
	3rd	Simple Assembly language Programming
	4th	
	5th	

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WEEK	Class Day	Theory Topics
7	1st	Microcontroller (Architecture) 8 bit
	2nd	Distinguiss between Microprocessor & Microcontroller
	3rd	8-bit & 16 bit
	4th	CISC & Risc Processor
	5th	Architecture of 8051 Microcontroller
8	1st	Signal description of 8051 Microcontroller
	2nd	Memory Organisation - RAM Structure.
	3rd	Registers, timers, Interrupts of 8051 MC
	4th	Addressing modes of 805,
	5th	Serial Communication.
9	1st	Addition of two 8 bit Number
	2nd	Define opcode, operand, T-state
	3rd	Fetch cycle, Machine cycle, Instruction cycle
	4th	Discuss timing diagram
	5th	Draw a Neat Sketch for 8085 MOV, MVI

WEEK	Class Day	Theory Topics
10	1st	Define Mapping
	2nd	diagram of I/O Read, I/O write Machine cycle
	3rd	Concept of interfacing
	4th	concept of Mapping
	5th	Data transfer Mechanisms
11	1st	Concept of ADC & DAC
	2nd	Programmable Periph, Interface 8255
	3rd	Concept of Memory Interfacing
	4th	Concept of Address decoding For I/O devices
	5th	Stack & Subroutines Programes
12	1st	code conversion BCD Arithmatic
	2nd	Looping, counting Idexing
	3rd	Assembly language Program for compare Maximum no in array.
	4th	Time delay & Calculation.
	5th	Counter & calculation one Register Pair.

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WEEK	Class Day	Theory Topics
13	1st	Maximum mode & timings
	2nd	7 segment LED display
	3rd	Interfacing stepper motor
	4th	Minimum mode & timings
	5th	ADC & DAC with Interfacing
14	1st	Masking of interrupt
	2nd	General square waves on all lines of 8255
	3rd	Interrupts & Interrupt Services
	4th	Define mapping
	5th	Simple Assembly language Programming
15	1st	data transfer Mechanisms.
	2nd	
	3rd	
	4th	
	5th	